	Application No.	Applicant(s)	
Notice of Allowability	10/721,203	VARGAS, ROY L.	
	Examiner	Art Unit	
	James Cho	2819	
The MAILING DATE of this communication appe All claims being allowable, PROSECUTION ON THE MERITS IS (herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIG	(OR REMAINS) CLOSED in this app or other appropriate communication GHTS. This application is subject to	olication. If not include will be mailed in due	ed course. <b>THIS</b>
1. 🖂 This communication is responsive to the application filed 1:	<u>1-25-2003</u> .		
2. The allowed claim(s) is/are <u>1-12</u> .			
3.  The drawings filed on <u>25 November 2003</u> are accepted by	the Examiner.		
<ul> <li>4.  ☐ Acknowledgment is made of a claim for foreign priority un a) ☐ All b) ☐ Some* c) ☐ None of the:  1.  ☐ Certified copies of the priority documents have 2.  ☐ Certified copies of the priority documents have 3.  ☐ Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)).  * Certified copies not received:  Applicant has THREE MONTHS FROM THE "MAILING DATE" of noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.</li> <li>5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submit INFORMAL PATENT APPLICATION (PTO-152) which give (a) ☐ including changes required by the Notice of Draftsperson 1) ☐ hereto or 2) ☐ to Paper No./Mail Date  (b) ☐ including changes required by the attached Examiner's Paper No./Mail Date  Identifying indicia such as the application number (see 37 CFR 1. each sheet. Replacement sheet(s) should be labeled as such in the cathed Examiner's comment regarding REQUIREMENT for the comment of the comment of the cathed Examiner's comment regarding REQUIREMENT for attached Examiner's comment regarding REQUIREMENT for the cathed Examiner's cathed Examiner's cathed Examiner's cathed Examiner's cathed Examiner's</li></ul>	been received.  been received in Application No cuments have been received in this is of this communication to file a reply of this application.  Itted. Note the attached EXAMINER' as reason(s) why the oath or declarate the submitted. on's Patent Drawing Review (PTO-1) as Amendment / Comment or in the Office of BIOLOGICAL MATERIAL in	complying with the red S AMENDMENT or Nation is deficient.  948) attached  office action of the sin the front (not the sin the submitted. It	quirements
Attachment(s)  1. ☑ Notice of References Cited (PTO-892)  2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)  3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/06 Paper No./Mail Date  4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	5. ☐ Notice of Informal P 6. ☐ Interview Summary Paper No./Mail Dat 7. ☐ Examiner's Amendn 8. ☑ Examiner's Stateme 9. ☐ Other	(PTO-413), e nent/Comment	

## **DETAILED ACTION**

## Allowable Subject Matter

Claims 1-12 are allowable over the prior art of record.

The following is an examiner's statement of reasons for allowance: Although Taylor et al. (US PAT No. 6,157,206) teaches an on-chip reference termination device, one of ordinary skill in the art would not have been motivated to modify the teaching of Taylor et al. to further includes, among other things, the specific of a reference current source to provide a reference current applied across the adjustable on-chip impedance and a comparator coupled to sense and compare across the adjustable on-chip impedance and a reference voltage, as set forth in the claims.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Wang et al. (US PAT No. 6,603,329) discloses systems and methods for on-chip impedance termination.

Bui et al. (US PAT No. 6,836,144) discloses programmable series on-chip termination impedance and impedance matching.

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to James Cho whose telephone number is 571-272-1802. The examiner can normally be reached on M-F 6:30 AM - 3:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mike Tokar can be reached on 571-272-1812. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

James H. Cho Primary Examiner Art Unit 2819

Jamo A Cho

Date: 4-15-2005